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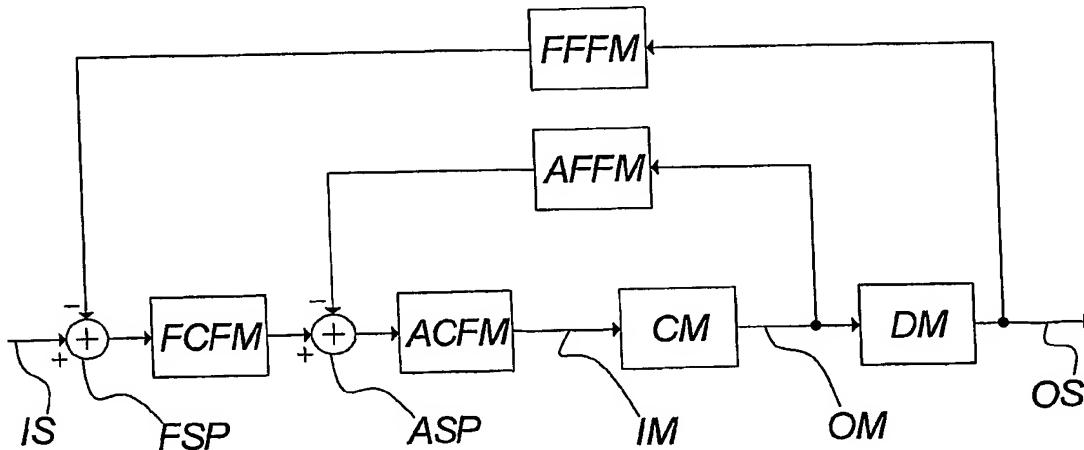
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(54) Title: SELF-OSCILLATING POWER CIRCUIT



WO 2005/002050 A1

(57) Abstract: The invention relates to a self-oscillating circuit comprising comparator means (CM) comprising at least one input means (IM) and at least one output means (OM), at least one of said at least one output means (OM) is coupled to at least one of said at least one input means (IM) via at least one filtering means (FM), said at least one filtering means (FM) at least partly comprising demodulation means (DM), wherein said filtering means (FM) is of at least fifth order.